DESIGNCON® 2025
WHERE THE CHIP MEETS THE BOARD

JANUARY 28 - 30, 2025
Santa Clara Convention Center
Santa Clara, CA

MEET HIGH-CALIBER ENGINEERS & EXECUTIVES IN SILICON VALLEY

DesignCon is the largest gathering of chip, board, and systems designers in the US. For 30 years, DesignCon has brought together the tight-knit semiconductor and electrical engineering community for three days of sourcing and knowledge sharing in the heart of high-tech innovation: Silicon Valley.

5,000+
Total Event Attendance

171
Exhibiting Companies

73%
are from California

83%
recommend, specify, or make the final buying decision

74%
have an active project or one planned within the next 12 months

Top Attendee Job Functions

Electrical Engineer
Design Engineer
Hardware Engineer

> " DesignCon is the event for practicing engineers in the SI, high speed serial data and now inclusive of some optical high speed serial data and automotive SI work. "
>
> – **Pavel Zivny**, *Tektronix*

Top 15 Attending Companies

Amazon	Astera Labs	Intel
AMD	Broadcom	Keysight Technologies
Amphenol	Cadence	Marvell
Ansys	Cisco	Meta
Apple	Google	Nvidia

> " There is no better event for education, networking, and community related to SI and PI. The conference is worth the money to those with a strong interest in SI and PI— even if paid with personal money. "
>
> – **Dan Chirpich**, *Re:Build AppliedLogix*

Ready to Meet Electronics Decision Makers at DesignCon 2025?

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877-882-3421

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Source: DesignCon 2024 Verified Attendee Registration Report and 2024 Post-show Attendee Survey